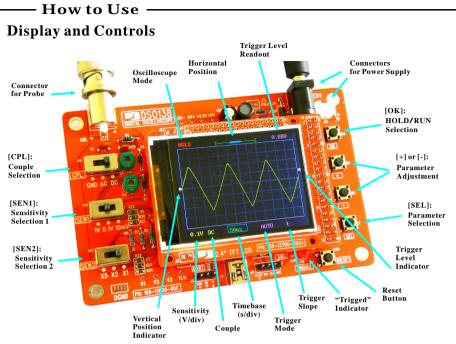


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22. LCD Board 20. Test signal ring 21. JP3 **Finished** look 1) Make a small ring with a Note: Install to the side Short JP3 with solder lead cut-off. opposite to LCD panel. floif 2) Solder the ring to the two 🗌 J1 :2 X 20 pin holes of J2 (as shown in the photo). ☐ J2, J3 :1 X 2 pin **Notes:** 1) JP1, JP2, JP5, and JP6 at bottom side should be kept open for normal running mode. Step 2 Test and Use 2) The USB connector do not have function. It was provided for future or user own use. 3) A 9V DC power supply (> 200mA capacity) is required to run the scope. Power supply is not included in the kit. A. Check voltages B. Attach LCD board C. Verify A. Use Apply 9V power to J10 (or J9). (1) Connect power supply again. You should see LCD () Attach probe clips to J1. Plug LCD board into the female headers J3, J7, and J8 lights up and oscilloscope panel displayed. (2) Check voltage at TP22. It should be around +3.3V. on the main board. ① Touch the red clip with your finger. Do you see signal 2 Press various buttons and move switches to verify (3) If voltage at TP22 is good disconnect power. Short JP4 from your finger? with solder permanently. their functions. (1)"Trigger" LED blinking twice 3 indicates booting-up is good. Troubleshooting NOTE 1: The voltages in the photo are for reference only. The voltages on your board could be different. LCD Dark No Display But they should be close to the values shown. Voltage NOTE 2: (No backlight) -5.0V (4.99V (3.3V) (5.02V) LED will be blinking constantly if MCU References (U1) can not detect valid LCD controller. 6.43V 9.39V (Input Voltage) Short JP4 if it has not been Is voltage at V+ 0.19V Check power Check LCD pin-header soldering. done. See Step 2 above. good? supply Yes. 8.60V* No ► Check +3.3V voltage -8.11V Is R36 value correct Press SW8. Does LED blink? → Check Y1, C12, C13 ► Fix R36 NOTE 3: and soldered good? Yes Check LED installation Make sure U1 and LED working -8.08V Yes 3.3V (you see LED blinks twice at pressing Check J3 soldering for Do you get about 3V between J1 pin 16 & Check R36 and RESET) before using Test Mode. possible opens or shorts Test Mode PIN 1 ► Check J1 soldering on LCD 8.34V power again What it is and how it works 18 on LCD board? board for possible opens Test Mode is used to find out possible opens (for all port pins) and shorts Yes -1.39V or shorts (use Test Mode) (for pins PB0 - 15 and PC13-15). When entered it first checks PB and PC pins with special patterns to find out possible shorts. If found LED will be Check LCD 2.16V fast blinking. Otherwise, it generate 3.3V and 0V alternatively at each port board No Trace pins (PA, PB, PC and PD) in cycle of about 4 seconds. These signals can be 0.81V used to check for opens. Visit forum for detailed Are the values of AV+ and How to us AV- correct? troubleshooting guide 1. Hold down SW4 and press RESET to enter Test Mode. Yes 2. If you see LED fast blinking that means there are shorts on PB or PC pins. Set CPL switch to GND and measure V1 and V2. No Check U2B, U2C and related parts around ** ** You need to find out the shorts first. 0V 1.66V NOTES: 3. If you see LED slowly blinking use a volt-meter to check each pin related *: These voltages are input voltage dependent. The values Are they correct? these two amplifiers connections that are suspected open. When you don't see voltage change shown were measured when input voltage was 9.39V. Yes Place the negative pen of volt-meter **: These voltages are measured when CPL switch (SW1) at a spot which is supposed being connected to a port pin there may be here to do voltage measurements. Check R12 and C8 is set to GND position open between the spot and the port pin.

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Connections

Attention

 Power Supply:
 Connect DC power supply to J9 or J10. The power supply voltage must be in the range of 8 - 12V.

 Probe:
 Connect probe to J1.

 Power supply voltage must not exceed 12V. Otherwise U5 will get hot.
 Allowed maximum signal input voltage is 50Vpk (100Vpp) with the clip probe.

Operations

Press on [SEL] button: Select parameter to be adjusted. The selected parameter will be highlighted. Press on [+] or [-] button: Adjust the parameter selected by [SEL] button.

Press on [OK] button: Freeze waveform refresh (entering HOLD state). Press on it again will de-freeze.

Change [CPL] switch: Set couple to DC, AC, or GND. When GND is selected the scope input is isolated from input signal and connected to ground (0V input).

Change [SEN1] or [SEN2] switch: Adjust sensitivity. The product of [SEN1] and [SEN2] settings makes the actual sensitivity which is displayed at the lower-left corner of the panel.

Press on [Reset] button: Perform a system reset and re-boots the oscillscope.

– Tips Vpos Alignment

This is to fix the mismatch between 0V trace and VPos indicator. To do this set couple switch [CPL] to GND position. Press on [SEL] button to make VPos indicator highlighed. Hold down [OK] button for about 2 seconds. You will see VPos indicator aligned to 0V trace when you release [OK] button. You may see some residue mismatch remains at the highest sensitivity settings. This is normal.

Restore Factory Default

Hold down [+] and [-] buttons simultaneously for 2 seconds.

Auto-center Trigger Level

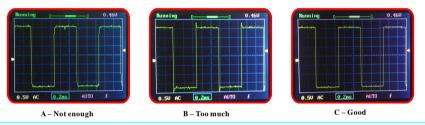
Highlight trigger level indicator and hold down [OK] button for 2 seconds.

Auto-center Horizontal Position Highlight HPos indicator and hold down [OK] button for 2 seconds.

Probe Calibration ecause there is always some capacitance between scope i

Because there is always some capacitance between scope input and ground probe needs to be calibrated to achieve better measurement results for high frequency signals. This can be done with the help of the built-in test signal. To do this please follow the steps below.

- 1. Connect the red clip to the test signal terminal and leave the black clip un-connected (see photo at right).
- 2. Set [SEN1] switch to 0.1V and [SEN2] switch to X5. Set [CPL] switch to AC or DC.
- 3. Adjust timebase to 0.2ms. You should see waveform similar to that shown in photos below. If traces are not stable adjust trigger level (the pink triangle on right screen border) so as you get a stable display.
- 4. Turn C4 (capacitor trimmer) with a small screw driver so that the waveform displays sharp rightangle (photo C).
- 5. Set [SEN1] switch to 1V and [SEN2] switch to X1while keep all other settings unchanged. Adjust C6 so that sharp rightangle waveform is displayed.



Turn On/Off Readouts

Press [SEL] so that timebase is highlighted. Hold down [OK] button for about 2 seconds. This will turn on/off measurement readouts.

Waveform Save/Recall

Press [SEL] & [+] simultaneously: Save currently displayed waveform to non-volatile memory. Press [SEL] & [-] simultaneously: Recall saved waveform

Triggers and Their Modes

Triggers are events that indicate signal voltage acrossing a set level (i.e. trigger level) along a specified direction (i.e. trigger slope, rising or falling). Oscilloscope uses triggers as reference points in time for stable waveform display and measurements.

Auto Mode

In auto mode oscilloscope will perform display refresh no matter triggers happen or not. When triggers are detected waveform display will be displayed with reference to trigger points. Otherwise, display waveform at ramdom reference points.

Normal Mode

In normal mode oscilloscope will only perform display refresh when there are triggers. If no triggers happen waveform display will stay unchanged.

Single Mode

Single mode is the same as normal mode except that oscilloscope will enter HOLD state after a trigger has been detected and waveform display has been updated.

Normal and single modes are useful for capturing sparse or single waveform.

Specifications	
Max realtime sample rate	1MSa/s
Analog bandwidth	0 200KHz
Sensitivity range	10mV/div - 5V/div
Max input voltage	50Vpk (1X probe)
Input impedance	1M ohm/20pF
Resolution	12 bits
Record length	1024 points
Timebase range	500s/Div 10us/Div
Trigger modes	Auto, Normal, and Single
Trigger position range	50%
Power supply	9V DC (8-12V)
Current consumption	~120mA
Dimension	117 x 76 x 15mm
Weight	70 gram (without probe)

Leave black clip

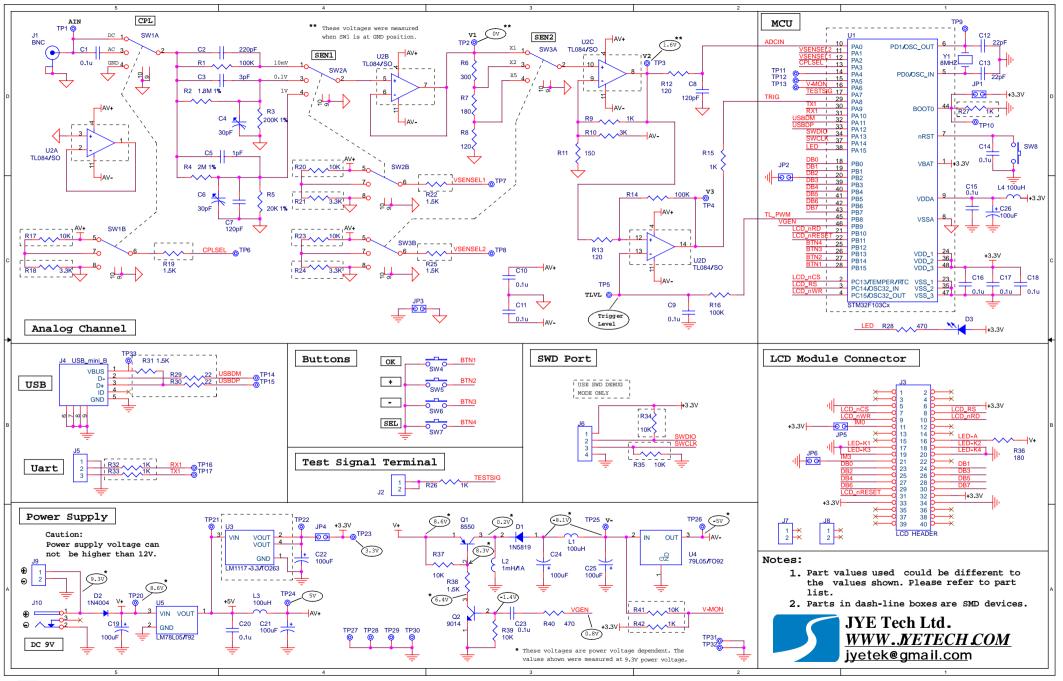
un-connected

Connect red clip to

test signal output

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